



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:	)	
Robert Chau et al.	)	
Serial No.: 10/618,226	)	Examiner: Not yet assigned
Filed: July 11, 2003	)	Art Unit: Not yet assigned
For: A METHOD FOR MAKING	)	Attorney Docket No: P12759C
A SEMICONDUCTOR	)	
DEVICE HAVING A	)	
<u>HIGH-K GATE DIELECTRIC</u>	)	
Which is a Continuation of Application:	)	
Serial No.: 10/082,530	)	Examiner: C. Lee
Filed: February 22, 2002	)	Art Unit: 2825

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

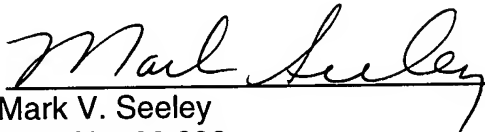
This Information Disclosure Statement is being submitted under 37 C.F.R. §1.97(b). Enclosed is a copy of Information Disclosure Citation Form PTO-1449 together with copies of the references cited on that form. It is respectfully requested that the cited references be considered and that the enclosed copy of Information Disclosure Citation Form PTO-1449 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

Pursuant to 37 C.F.R. § 1.97, the submission of this Information Disclosure Statement is not to be construed as a representation that a search

has been made and is not to be construed as an admission that the information cited in this statement is material to patentability.

Respectfully submitted,

Dated: September 2, 2003

  
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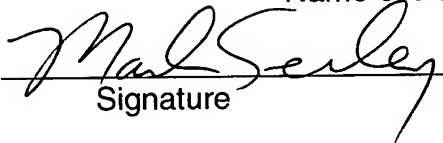
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